PATENT ABSTRACTS OF JAPAN

(11)Publication number:

09-230311

(43)Date of publication of application: 05.09.1997

(51)Int.Cl.

GO2F 1/133 G02F 1/1343 G02F 1/136 H01L 29/786

(21)Application number: 08-317140

(71)Applicant: SEMICONDUCTOR ENERGY LAB CO LTD

(22)Date of filing:

13.11.1996

(72)Inventor: HIRAKATA YOSHIHARU

YAMAZAKI SHUNPEI

(30)Priority

Priority number: 07321075

07350169

Priority date: 14.11.1995

23.12.1995

Priority country: JP

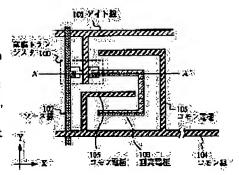
JP

(54) DISPLAY DEVICE

(57)Abstract:

PROBLEM TO BE SOLVED: To prevent a pixel electrode from interfering in another signal line in a display device applying electric field in a substrate surface.

SOLUTION: In the configuration of an active matrix type pixel, a common electrode 105 prolonged from a common line 104 held to prescribed potential in the same plane and the pixel electrode 103 becoming a companion to it and connected to a drain of a thin film transistor 100 are arranged in an eddy shape so as to be engaged each other. Then, a liquid crystal is responded by the electric field nearly parallel to a substrate formed between both electrodes 103, 105, and a display is performed. Further, by surrounding the pixel electrode 103 with the common electrode 105, it is prevented that the pixel electrode 103, a gate line 101 and a source line 102 are mutually interfered.



LEGAL STATUS

[Date of request for examination]

20.08,1999

[Date of sending the examiner's decision of

17.02.2003

[Kind of final disposal of application other than the

withdrawal

examiner's decision of rejection or application

converted registration]

[Date of final disposal for application]

21.04.2003

[Patent number]
[Date of registration]
[Number of appeal against examiner's decision of rejection]
[Date of requesting appeal against examiner's decision of rejection]
[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office